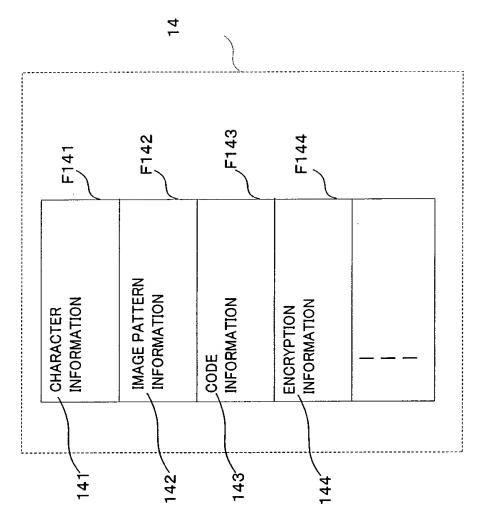
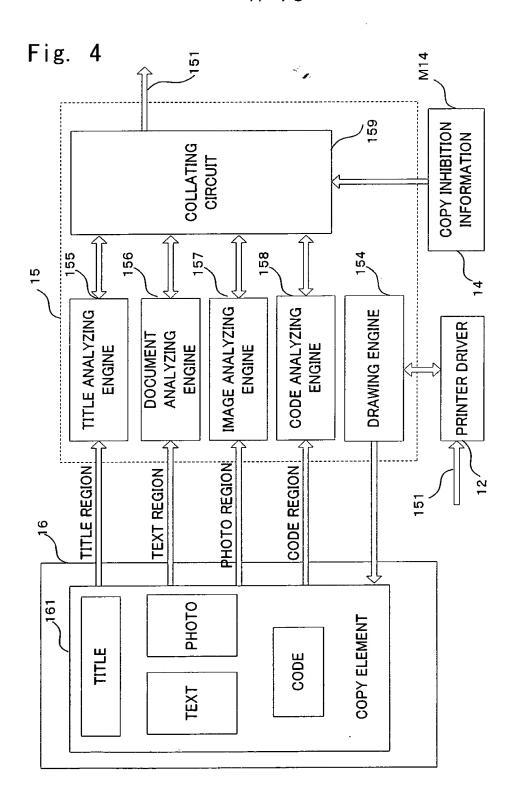
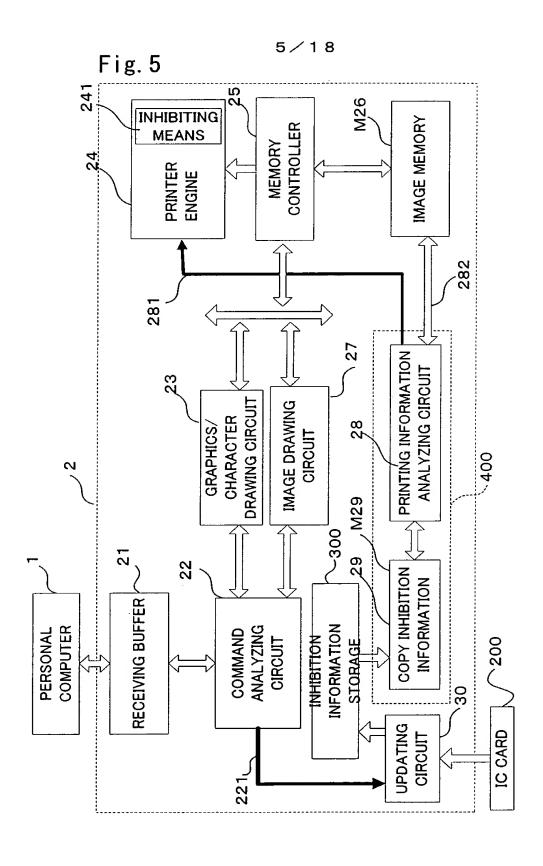
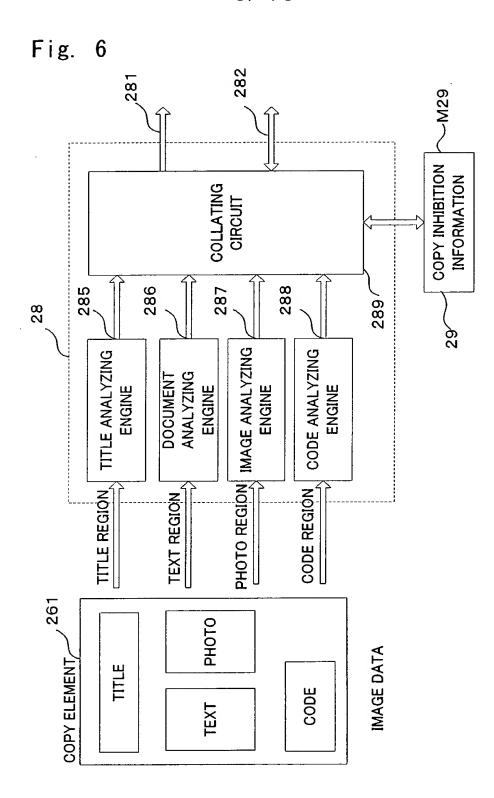


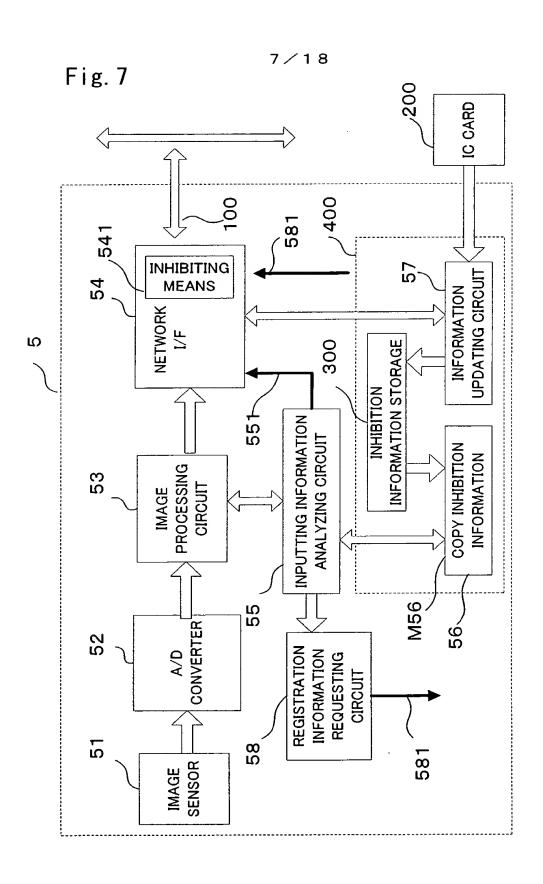
Fig. 3



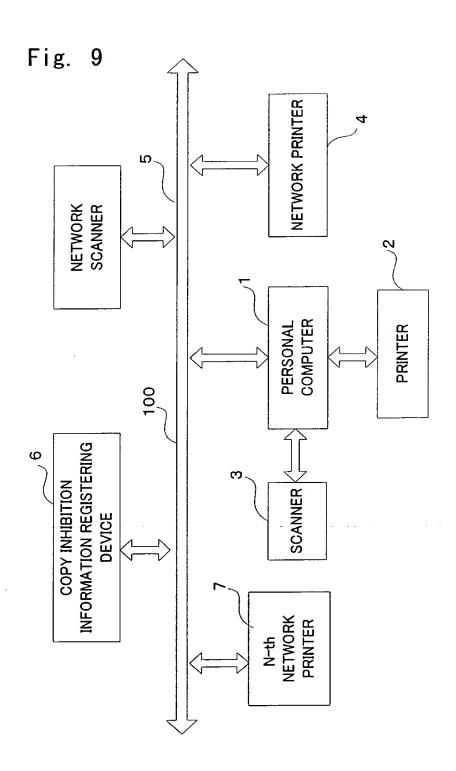




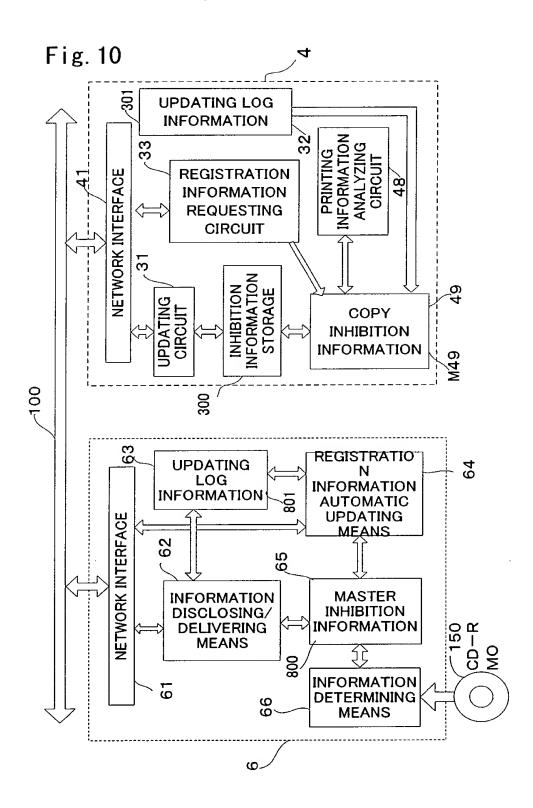


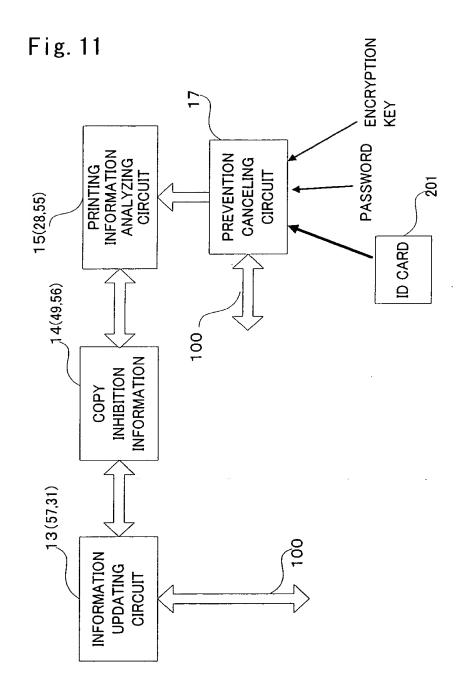


8/18 Fig. 8 M46 INHIBITING MEANS IMAGE MEMORY CONTROLLER MEMORY 44 PRINTER ENGINE 100 31 UPDATING **CIRCUIT** INHIBITION INFORMATION STORAGE GRAPHICS/ CHARACTER DRAWING CIRCUIT , M49 IMAGE DRAWING CIRCUIT COPY INHIBITION INFORMATION 300 INFORMATION
ANALYZING CIRCUIT 100 ANALYZING INTERFACE COMMAND NETWORK **CIRCUIT** 48



7





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Fig. 12

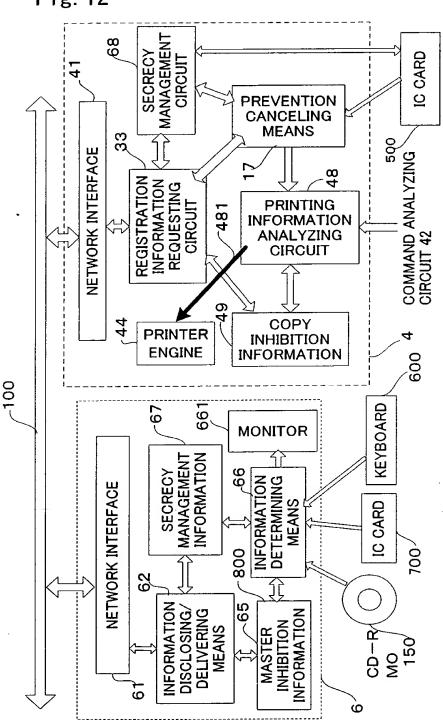
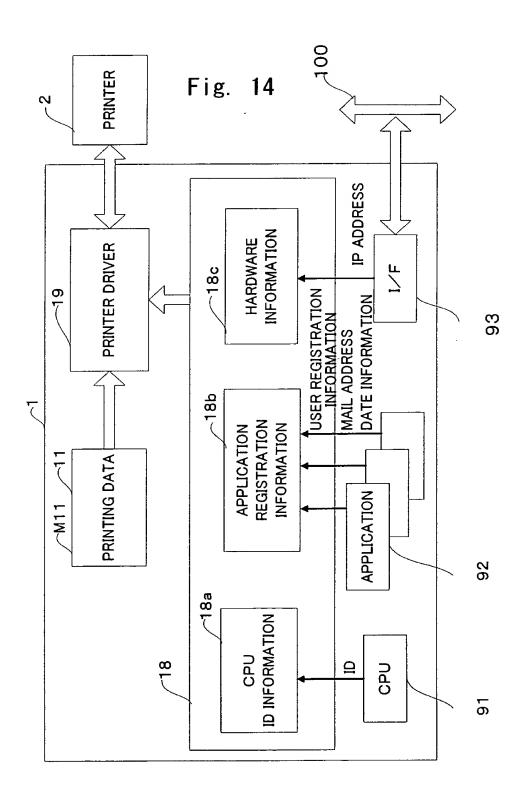


Fig. 13

670	INDIVIDUAL ID	ID1/PW1			ID1/PW1		ID1/PW1		IDn/PWn
	GROUP ID	00X	00X	00X	Y00	200	A00		Gnn
	PERMIT LEVEL	A001	B001	C001	C002	B002	A002	• •	JVI_
	INFORMATION ID	D001	D002	5000	D004	5000	9000		Dnnn



4

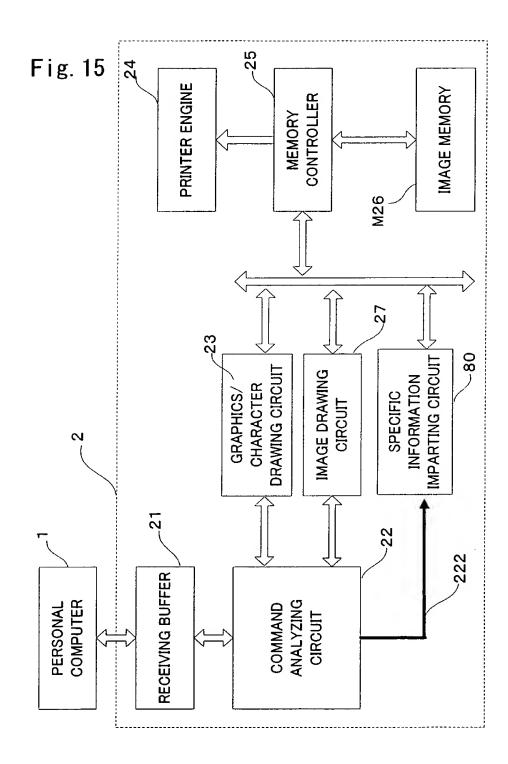


Fig. 16

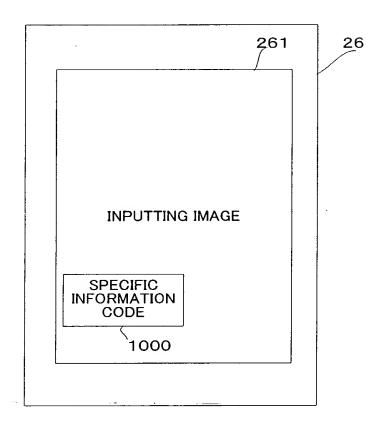


Fig. 17

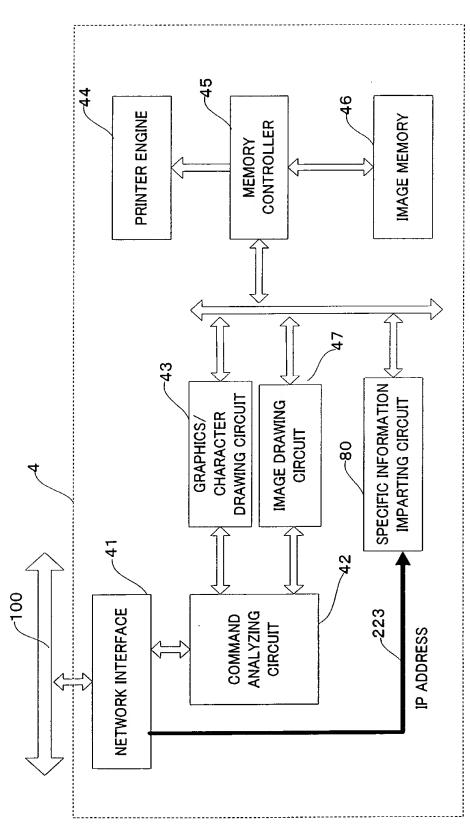


Fig. 18

